

CELQ-033-POL-7
Celestica Environmental Requirements
for Purchased Components

Revision 4
2007-04-17
(This "Policy")

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1 Purpose

This Policy defines Celestica's environmental requirements to our suppliers. The intent of the policy is to assist Celestica's customers in complying with RoHS (defined below) and WEEE (defined below) type directives, such as those promulgated by the European Union and the People's Republic of China, along with other similar existing or emerging legislation or regulations. It also defines minimum survivability standards for electronic components along with the manufacturability data that Celestica requires in order to use said parts.

Celestica reserves the right to amend this Policy from time to time.

2 Scope

This Policy applies to all purchased goods intended for use in electronic products manufactured by Celestica, with the exception of chemicals and consumables¹. It encompasses all of Celestica's sites and affects Celestica's entire global supply base. Goods that must comply with a RoHS-type directive will be identified in the relevant QSPEC document (defined below). Furthermore, goods that must be lead (Pb)-free (defined below) will be identified by a reference to this effect in the QSPEC. Any exceptions to this Policy will be documented in the applicable QSPECs. These exceptions may add to the requirements for a particular part number.

This Policy applies to, but is not limited to, the following commodity categories:

Actives
Passives
Optics
IC's
Magnetics
Memory
Logic
Printed Wiring Boards
Connectors
Plastics
Mechanicals
Peripherals
Sub-assemblies

3 Responsibilities

The supplier shall ensure that all goods supplied to Celestica fully comply with the requirements of this Policy.

3.1 Franchised Distributors

The requirements set out in this policy are applicable to original component manufacturers, and as such cannot reasonably be met by our Approved Component Distributors. However, Celestica does require our Distribution partners to assist in our compliance requirements through the following actions:

- Distributors must establish & maintain internal processes to identify, segregate and store RoHS compliant components, so as to minimize the opportunity to mix compliant with non-compliant materials. Celestica encourages suppliers to demonstrate compliance capability through the use of external accreditation. (e.g. EIA/ECCB-954, BSI Kitemark)
- Distributors must take reasonable steps to pick & ship components that meet the requirements of Celestica quality Specifications (QSPECs). Where the QSPEC specifies date code restrictions, these

¹ Chemicals and consumables are covered by [Celestica Environmental Requirements for Chemicals/Consumables](#), CELQ-033-POL-15

must be adhered to, as some suppliers are not changing manufacturer part numbers when their components become compliant.

- The manufacturer's part number (& date code) referenced on Celestica's Purchase Order (P.O.) and QSPEC will take precedence over any conflicting information, although Celestica expects suppliers to make best endeavors to notify us of any discrepancies noted in our purchasing documentation.
- The P.O. will identify whether an ordered part needs to comply with this Policy. Should supplier have reason to believe a component does not meet the requirements of this Policy, they should immediately inform their Celestica purchasing contact for resolution.
- Distributors must take reasonable steps to provide Celestica with compliance documentation from the original part manufacturer, including material declarations and certificates of compliance, per the Documentation Requirements below.

4 Definitions

Bright Tin (Sn)	Bright Tin (Sn) has grain size less than 1µm and a carbon content of greater than 0.2% in the plating.
Chemicals and Consumables	Items used in the manufacture of electronics which are not generally considered functional components of the final product. Examples include solder paste, flux, inks, etc. Celestica's environmental requirements for chemicals and consumables are covered in CELQ-033-POL-15.
EIA	Electronic Industries Alliance
Homogeneous Material	Means a material that cannot be mechanically disjointed (separated) into different materials.
Homogeneous	Means a good with uniform composition throughout.
iNEMI	International Electronics Manufacturing Initiative, Inc
IPC	Institute for Interconnecting and Packaging Electronic Circuits
J-STD	Joint IPC/JEDEC standard document
JEDEC	Joint Electron Device Engineering Council
JESD	JEDEC standard document
Lead (Pb)-Free	Means that a good does not contain lead (Pb) above the material limit specified by RoHS
Lead (Pb)-Free Compatible	Means a good does not contain lead (Pb) <i>and</i> can withstand the lead-free processing requirements as documented in the Minimum Process Requirements section
Matte Tin (Sn)	Matte Tin (Sn) has grain size greater than 1µm and a carbon content of less than 0.05% in the plating layer. Thickness of the matte Tin (Sn) layer should be equal to or greater than 7.62 µm except where specified otherwise in the Materials Requirements section.
MSL	Moisture Sensitivity Level (per J-STD-020)
PCN	Process Change Notification

QSPEC	Celestica's Quality Specifications applicable to each good
RoHS	Means the European Union's directive on the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (Directive 2002/95/EC). May also be used to describe similar legislations from other jurisdictions, such as China, Korea, and California.
RoHS Compatible	Means a good that complies with the materials restrictions specified by Directive 2002/95/EC <i>and</i> can withstand the lead-free processing requirements as documented in the Minimum Process Requirements section
RoHS Compliant	Means a good that complies with the materials restrictions specified by Directive 2002/95/EC
RoHS Exempt	Means a good that complies with Directive 2002/95/EC by qualifying for an exemption listed in the Annex of Directive 2002/95/EC; or where a goods' end application has been ruled exempt from RoHS or WEEE by an authority of competent jurisdiction
Waiver	Means a Celestica standard format used to document deviations from the specifications as written on the QSPEC. A valid waiver must be signed by the appropriate authorized Celestica representative
WEEE	Means the Waste Electrical and Electronic Equipment (Directive 2002/96/EC)

5 References

The following are incorporated into and made part of this Policy:

- QSPEC [available on the Celestica website www.celestica.com, through the Partner Web Portal]
- Celestica Packaging and Handling Specification (PK0763) [available on the Celestica website www.celestica.com]*
- Celestica Bar Coding Standard for Procured Production Goods (CELQ-033-STD-51) [available on the Celestica website www.celestica.com]*
- Celestica Notification of Product/Process Changes, Discontinued Availability and Product Alerts by Suppliers (CELQ-033-POL-2) [available on the Celestica website www.celestica.com]*
- Celestica RoHS Certificate of Compliance Form (CELQ-033-FORM-18)
- Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment
- Management Methods for Control of Pollution Caused by Electronic Information Products, Ministry of Information Industry Order #39 (China RoHS), and related documents
- RosettaNet Technical Dictionary (V4.1.1)
- IPC/JEDEC Moisture/Reflow Sensitivity Classification for Non-hermetic Solid State Surface Mount Devices (J-STD-020C)
- IPC/JEDEC Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices (J-STD-033B)
- Evaluation Procedure for Determining Capability to Bottom Side Board Attach by Full Body Solder Immersion of Small Surface Mount Solid State Devices (JESD22A111)
- Stress Test Driven Qualification Specification (JESD47D)
- Customer Notification of Product/Process Changes by Semiconductor Suppliers (JESD46)
- EMS Forum Guidelines for Suppliers Transitioning to RoHS Compliant Components (Rev. 2.0)
- EIA Joint Industry Guide (JIG) – Material Composition Declaration Guide (JIG-101)

- iNEMI Tin Whisker User Group Recommendations on Lead-Free Finishes for Components Used in High-Reliability Products (May 2005)
- Telecom/Hi-End Computing Tin Whisker Concerns & Test Requirements for Components Specification
- Measuring Whisker Growth on Tin and Tin Alloy Surface Finishes (JESD22A-121)
- Environmental Acceptance Requirements for Tin Whisker Susceptibility of Tin and Tin Alloy Surface Finishes (JESD201)
- iNEMI Tin Whisker Acceptance Test Requirements (July 28, 2004)
- Current Tin Whisker Theory and Mitigation Practices Guideline (JP002)
- JEDEC Marking, Symbols and Labels for Identification of Lead-Free Assemblies, Components and Devices (JESD97)²
- IPC Marking, Symbols and Labels for Identification of Lead-Free and Other Reportable Materials in Assemblies, Components and Devices (IPC-1066)²
- IPC Materials Declaration Management (IPC-1752)
- Celestica Printed Wiring Board Specification (CELQ-001-SPEC-2)
- EIA Electrical and Electronic Components and Products Hazardous Substance Free Standard and Requirements (EIA/ECCB-954)
- BSI Kitemark
- ASTM Standard Practice for Operating Salt Spray (Fog) Apparatus (ASTM B117)
- RoHS Enforcement Guidance Document (Version 1 – updated January 2007) from the UK’s Department of Trade and Industry (DTI)

* External Celestica Specifications can be found on the Celestica internet site:
<http://www.celestica.com/SupplyChain/SupplyChain.aspx?id=798>

Questions or concerns relative to these references should be highlighted to the appropriate Celestica contact for clarification. In cases where newer published revisions of the industry standards listed in this Policy are available, the newest revision shall be utilized by both Celestica and the Supplier. Celestica does not specify reference revision levels in this Policy unless required for clarity where a significant change has occurred as compared to the previous revision.

In case of conflict between Celestica requirements, the following order of precedence shall apply:

- Waiver
- QSPEC
- OEM Specification / Drawing
- This Policy
- Referenced Industry Standards

6 Revision History

Revision	Date	Updated By
Rev 0	March 26, 2004	Arshad Khan, Sean McDermott
Rev 1	June 9, 2004	Sean McDermott
Rev 2	August 18, 2004	Sean McDermott
Rev 3	November 7, 2005	Sean McDermott
Rev 4	April 17, 2007	Sean McDermott, Nathaniel Jeffries

² JESD97 and IPC-1066 are expected to be supplanted by J-STD-609. When this happens, all references in this policy to JESD97 and IPC-1066 shall be understood to include J-STD-609.

7 Qualification Requirements

7.1 Approved Environmentally Compliant Suppliers

Supplier will not supply any goods to Celestica unless it has confirmed with Celestica that it has been approved by Celestica as a supplier eligible to supply such goods to Celestica. Examples of such confirmation are appearing on the relevant QSPEC and/or purchase order (P.O.) for the part in question.

7.2 Qualification of Goods

The Supplier has the responsibility to perform internal qualification activities for each part number prior to sale to Celestica. Celestica will have the option, but not the obligation to take part in additional part or application-level qualification activities. This process may entail a review of applicable part information, part testing, application/product level testing, and compliance verification testing. Specific Supplier requirements for qualification will be detailed as needed. General informational items that will optionally be considered as part of Celestica's additional qualification activities are listed in the General section below, and upon request by Celestica will be made available by Supplier. Please note that Supplier part or process changes (documented as PCN's) may also require additional qualification activities by Celestica, therefore the following data must be available from the Supplier at the time the PCN is issued.

7.2.1 *General*

The Supplier will provide its internal qualification data to Celestica including, without limitation, information on the solder alloy used for testing, manufacturing process capability, and details on the termination metallurgy and structure of the part. The data should demonstrate both forward compatibility (ability to solder the parts using Lead (Pb)-free solders and the appropriate higher temperature profiles), and backward compatibility (ability to solder the parts using existing Tin/Lead (Sn/Pb) solder and profiles). There are exceptions such as for area array packages with Lead (Pb)-free solder balls, which may not be backward compatible. If known to the Supplier, these exceptions must be pointed out in the documentation. It is the Supplier's responsibility to ensure that the first level interconnect (i.e. within the component package) is compatible with known second level (i.e. board level) soldering processes (as defined by IPC/JEDEC J-STD-020, current revision).

7.2.2 *Test Methods*

Any new or changed materials or processes used in manufacturing the component shall be evaluated by the Supplier as per JESD47D (where applicable), or a close approximation thereof approved for use by Celestica. The qualification report provided to Celestica should include testing to the following reference standards where relevant and applicable.

1. Solderability testing (per IPC/EIA J-STD-002B or JESD22-B102D; documentation to be available upon request, but is not required to be provided to Celestica if the finish is listed as Acceptable for the corresponding package type in the Materials Requirements section below)
 - Both no-clean and aqueous clean solder paste and wave solder flux should be included.
2. Solder joint reliability testing (per IPC-A-9701)
3. Monotonic bend testing (per IPC/JEDEC-9702)
4. Mechanical shock and vibration (per AEC-Q100-Rev E/Mil-Std 883)
5. High temperature storage (per AEC-Q100-Rev E/JESD22-A103C)
6. Tin (Sn) Whisker growth testing:
 - Tin (Sn) Whisker testing is required for any solderable finish containing greater than 95% Tin (Sn). This applies whether or not the finish appears on the Acceptable Termination Finish list contained in this specification. Solder balls are not considered to be a plated finish and are exempt from such testing.
 - Whisker Testing must follow the test method requirements documented in JESD22A-121 (Test Method for Measuring Whisker Growth on Tin and Tin Alloy Surface Finishes). Whisker test evaluation should follow the requirements laid out in the document Environmental Acceptance Requirements for Tin Whisker Susceptibility of Tin and Tin Alloy Surface Finishes (JESD201). Celestica will utilize these acceptance criteria when evaluating the test results. Note that the required whisker testing Class requirement (per JESD201) will be documented on the Celestica QSPEC.

7. Moisture sensitivity level (per IPC/JEDEC J-STD-020C, as applicable)
8. Maximum Package Classification Reflow Temperature (per IPC/JEDEC J-STD-020C, Table 4-2)
9. Electrical Performance/Characteristics Requirements (per QSPEC)

8 Minimum Process Requirements

All Lead (Pb)-free or RoHS compliant parts covered by this Policy must meet or exceed the requirements of the following specifications (as applicable) in order to be compatible with Celestica's Lead (Pb)-free manufacturing processes. There will be no exceptions to these requirements unless they are expressly stated in the applicable QSPEC.

The Supplier shall not provide a quotation for a non complying part unless Celestica has provided an approved Waiver (defined above) which documents that the deviation from these requirements is acceptable.

If a Waiver is provided, the Supplier will provide the good with a revised part marking/labeling scheme permitting visual confirmation of non-compliant parts. The Revised Marking must be pre-approved by Celestica.

At a minimum, Supplier will comply (if applicable) with the following requirements:

1. All parts as delivered must meet the requirements of the Handling, Packing, Shipping and Use specification J-STD-033B.
2. Marking and Labeling of parts and packaging materials shall meet the requirements of JESD97/IPC 1066 (as applicable).
3. Supplier will provide a minimum of 90 days prior notice of a Product Change Notification (PCN) before the planned change implementation date. Any changes related to environmental compliance or manufacturing process are considered to be Major Changes as defined in JESD46-B. PCN approval will be performed as per the Celestica Notification of Product/Process Changes, Discontinued Availability and Product Alerts by Suppliers document (CELQ-033-POL-2)
4. For Printed Wiring Boards (PWBs), the supplier must meet the requirements outlined in the Celestica Printed Wiring Board Specification (CELQ-001-SPEC-2, current revision)
5. All components shall be capable of withstanding one dry rework condition (as defined in J-STD-020C) at a process temperature of 260°C. The MSL rating at this temperature shall be determined using the method listed in J-STD-020C. This rating shall be clearly indicated on the barcode or MSL Caution Label.
6. All parts must be capable of withstanding, without damage, the manufacturing process temperatures applied to the part during the soldering process. Depending on the soldering method utilized, the parts must be capable of meeting the appropriate minimum requirement indicated below:
 - All non-hermetic integrated circuit parts intended to undergo a surface-mount reflow process shall meet the Pb-Free process package classification reflow temperatures as defined in J-STD-020C Table 4-2. Conversely, hermetically-sealed integrated circuits or non-integrated circuit parts undergoing a surface-mount reflow process shall be capable of withstanding a maximum temperature of 260°C, and a sustained time of 150 seconds above 217°C.
 - All parts intended to undergo bottom-side full body wavesolder immersion shall be capable of withstanding immersion in 260°C solder for 10±1 seconds per JESD22A111. Celestica preference is that small passive, discrete, and solid state devices be capable of withstanding immersion temperatures of up to 265°C for 5 seconds to be compatible with Pb-Free manufacturing processes.
 - All parts intended to undergo a top-side wave soldering process shall be capable of withstanding a maximum lead-immersion temperature of 270±5°C for 7+2/-0 seconds without damage to the part body or housing material surrounding the leads. The parts shall also be capable of withstanding a maximum body temperature of 200°C (minimum) for 10±1 seconds.
 - All parts intended to undergo a manual soldering process shall be capable of withstanding a maximum temperature of 265°C for 10±1 seconds.

NOTE 1: All component temperatures listed above refer to component body temperatures, not lead/joint temperatures, unless stated otherwise.

NOTE 2: All soldered parts, regardless of soldering method used for attach, must be capable of withstanding a one-time dry rework condition using hot-air soldering as described in J-STD-020C section 4.1. This applies to parts which are not normally covered in the scope of J-STD-020C in lieu of an equivalent standard.

9 Logistical Requirements

9.1 PCN Submission

When any change occurs to parts possessing Environmental Requirements, it must be identified to Celestica through our documented PCN procedure CELQ-033-POL-2.

9.2 Part Identification

Part identification is critical for Lead (Pb)-free, RoHS compliance, or RoHS exemption conversions. The method used must be capable of distinguishing the revised parts from the prior versions of the same part. Preference will be given to industry-standard methodologies as they are established. Existing standards for descriptive terminology and marking are as follows:

- Terminology – RosettaNet RNTD Version 4.1.1
- Marking, Symbols, and Labels for Identification of Lead (Pb) Free Assemblies, Components, and Devices - JESD97
- Marking, Symbols and Labels for Identification of Lead-Free and Other Reportable Materials in Assemblies, Components and Devices - IPC-1066

Note: if a Lead (Pb)-free version of a part is followed by a RoHS compliant version, the method of identification must be able to distinguish the two variants. As such, when parts are converted to either or both of these compliance conditions, the PCN issued to Celestica must include the following information:

1. Method of Identification (see below for part numbering requirements)
2. Effective cutover date and date code (date and date code after which all parts shipped to Celestica will be converted, as indicated on the parts/packaging materials)
3. Lead (Pb)-free/RoHS logo usage, description, and location on part (refer to JESD97))
4. Part Labeling usage, description, and location (refer to JESD97)
5. Part technical specifications as required in the Qualification of Goods – Test Methods section
6. Part documentation as outlined in the Documentation Requirements section (below)

Note: If a supplier product/process change results in a PCN being issued to Celestica, and this PCN invalidates a Certificate of RoHS Compliance (C of C) previously issued to Celestica, the supplier shall notify Celestica of the status of the C of C. If the revised parts are RoHS compliant and subject to a new C of C, this document shall be made available to Celestica as part of this notification procedure.

9.2.1 *New Orderable Supplier Part Numbers*

Celestica's preferred method of identification is for the creation of new orderable supplier part numbers for any compliance-related part conversions. New orderable supplier part numbers are required under either of the following conditions:

- The component change requires a modification to Celestica's manufacturing processes – these are referred to as non-backwards compatible parts. (Refer to the EMS Forum Guidelines for Suppliers Transitioning to RoHS Compliant Components (current revision). Known non-backwards compatible scenarios include, but are not limited to, the following:
 1. Lead (Pb)-free Tin/Silver/Copper (Sn/Ag/Cu) solder balls used in eutectic Tin/Lead (Sn/Pb) assembly processes.
 2. Bismuth (Bi) contained in termination finishes of pin-through hole (PTH) components to be used in Tin/Lead (Sn/Pb) solder assembly processes.

3. Bismuth (Bi) or Lead (Pb) contained in termination finishes of pin-through hole (PTH) components to be used in Lead (Pb)-free wave solder processes.
- There is no date code traceability (note that date code information must be located on the component itself).
Exception - components deemed by Celestica to be too small to permit marking or date code information changes to be printed on the part, in which case this information shall be applied to the lowest-level packaging labels.

10 Documentation Requirements

10.1 Required Environmental Information

The Supplier shall provide the following information (as applicable) to Celestica upon request, or at the time of any part changes communicated through PCN's.

1. MSL rating at classification temperature (Per J-STD-020C)
2. Maximum component temperature (Package Classification Reflow Temperature - per J-STD-020C, Table 4-2) for assembly
3. Maximum processing temperature for rework (Per J-STD-020C)
4. Component mass information
5. Component termination structural and finish compositional details
6. Component Leadframe or Underplating Material
7. Component marking location (Per JESD97) and description/photo
8. Date code cutover information for Lead (Pb)-free or RoHS Compliant parts (whichever stage applies)
9. RoHS Compliance Certification (CELQ-033-FORM-18 or IPC-1752, see below)
10. Indication of requirements for any substance or part within the component to be selectively recycled according to WEEE Annex 2
11. Material composition details for Joint Industry Guide List A substances, and any List B substances above the listed threshold (Per IPC-1752, refer to Materials Requirements section)
12. Laboratory test results corroborating the materials declaration above (upon request from Celestica)

10.2 RoHS Compliance

RoHS Compliance within this policy encompasses both EU and China RoHS at the time of writing. The material and documentation requirements of this policy apply to both legislations for all parts. There may be related legal requirements placed upon the supplier that are not detailed within this policy. The supplier *must* meet all legal requirements of the jurisdiction to which their parts are shipped³.

Celestica will communicate RoHS component requirements in the QSPEC, and/or on the purchase order, (P.O.), When Celestica orders a RoHS compliant or compatible component, or alternatively if, through the PCN process, the Supplier informs Celestica of their plan to convert a component to be compatible with RoHS (either full compliance or as an exempt⁴ part), the Supplier shall provide part number-level documentation, as requested by Celestica. The documentation may include, but not be limited to, the items indicated below:

1. The first form of required documentation is a Certificate of RoHS Compliance, which must be generated, signed, and received by Celestica prior to delivery of any RoHS Compliant or exempt parts. Certificate of RoHS Compliance documents shall be sent to CofC@Celestica.com. This document is a legally binding declaration from the Supplier that the referenced parts meet all RoHS material requirements (refer to 2002/95/EC). The format of the Certificate of RoHS Compliance shall comply with IPC-1752 industry standard guidelines. Celestica recommends the use of the Certificate of Compliance format documented in CELQ-033-FORM-18, (which is in-line with IPC-1752 requirements), or the "Class 1 – RoHS Only" Declaration Type of IPC-1752-1.

³ Including the marking and labeling requirements of China RoHS, along with the obligation to disclose the presence of hazardous substances present within the suppliers' products.

⁴ Refer to Directive 2002/95/EC for the list of part applications that are exempt from the requirements of Article 4(1) which covers the banned materials.

2. The second form of documentation is a Materials Declaration, which states the location, content (by weight and ppm) of each substance present in the part at a homogeneous material level. Refer to the Materials Requirements section for more details. The reporting format to be used shall comply with IPC-1752 Class 5 or 6 industry standard guidelines. Materials Declarations to the appropriate IPC reporting class shall be provided to Celestica upon request.
3. In some cases, RoHS Compliance of a given part number may require verification through analytical testing, which may be performed by an internal or external laboratory. At Celestica's request, the Supplier shall provide Celestica with appropriate test data which demonstrates RoHS compliance of the part.

11 Materials Requirements

Celestica requires that all parts purchased with Environmental Compliance Requirements (as listed in the purchasing documentation) be fully compliant with the legal requirements of whichever indicated legislation applies, as they exist on the date of purchase. Materials declaration reports may be requested at any time from the supplier in order to determine whether the parts meet existing legal, customer, or market requirements.

Celestica's Materials Declaration reporting requirements for purchased components are derived from the RoHS Directive's Enforcement Guidelines Document (Updated – January 2007), EIA Joint Industry Guide (JIG) for Material Composition Declarations (JIG-101), and the IPC-1752 reporting standard format. When requested by Celestica, the supplier shall comply with IPC-1752 specifications' Class 5 requirements as a minimum, when this is not superseded by legal or customer-specific requirements as communicated within the request. All non-proprietary substances contained within the part must be declared at a ppm level within each homogeneous material, including the location and mass of the given substance. In specific cases, Celestica may specify additional requirements for the Declaration, such as negative or ppm declarations for additional materials as specified in JIG-101.

These reporting requirements apply to all parts covered by this Policy, unless specifically stated otherwise in the applicable Celestica QSPEC document.

11.1 Solderable Part Compatibility

All Lead (Pb)-free, RoHS compliant, or RoHS exempt parts possessing Environmental Compliance Requirements as indicated on the QSPEC must be metallurgically compatible with Celestica's manufacturing processes, as well as with the other materials used on the final assembly. Celestica's termination finish recommendations are in line with iNEMI's for the purpose of mitigating tin whisker concerns, as outlined in the document: Interim Recommendations on Lead-Free Finishes for Components Used in High-Reliability Products version 3 (updated May 2005). Considerations for other reliability issues have also been incorporated into the requirements outlined below.

Note that since a full understanding of the tin whisker growth mechanism is still lacking, collecting data on the characteristics of tin platings is critical to help increase the knowledge level on parameters affecting tin whisker growth. Celestica requires tin whisker test data be provided for any new/changed solderable finishes which utilize greater than 95% tin by weight. Solder balls are not included in this requirement. When required, Celestica mandates that the tin whisker testing shall be performed in accordance with the JEDEC standard JESD22A-121. Note that the required whisker testing Class (per JESD201) for the particular part will be documented on the Celestica QSPEC. Refer to Current Tin Whisker Theory and Mitigation Practices Guideline (JP002) for more information on control of tin whiskers.

12 Recommended Termination Finishes

The following sections document Celestica's current termination finish recommendations based on type of part (package style & technology). Commonly used finishes have been categorized as either Acceptable or Unacceptable. Celestica will neither recommend to our customers, nor to our own design community, any part bearing an Unacceptable finish for use in RoHS Compliant products. Furthermore, Celestica will make every effort to eliminate the use of such parts in cases where they are already specified. Neither

classification implies that supplier or Celestica qualification or reporting requirements have been waived based on this categorization, in all cases the governing Celestica purchasing specifications apply. Relevant data may be requested for any finish in order to confirm compatibility with Celestica's product or process requirements.

12.1 Electronic Components

Acceptable termination finish formulations (subject to customer approval):

- Non-tin plating: Nickel/Palladium/Gold or Nickel/Palladium (Ni/Pd/Au or Ni/Pd).
- Matte tin (Sn) plating over a Nickel (Ni) underlayer. Components that use Nickel underplating should have a minimum porosity-free Nickel thickness of 0.5 μm with 1.27 μm or greater thickness recommended. (Tin thickness over Ni must be $> 2 \mu\text{m}$).
- Matte Tin (Sn) over a Silver (Ag) underlayer is a potentially acceptable finish with acceptable test results. Minimum required Silver underlayer thickness is 2 μm . (Tin thickness over Ag must be $> 2 \mu\text{m}$).
- Fused Tin (Sn) plating. Fusing is a reflowing operation usually done by dipping the Tin-plated surfaces into a hot oil bath shortly after plating. Preferred over nickel barrier (2 μm recommended nickel thickness).
- Hot dipped Tin/Silver (Sn/Ag), Tin/Copper (Sn/Cu), or Tin/Silver/Copper (Sn/Ag/Cu). This is a molten tin bath process generally used for structural steel parts, connectors or relays.
- Matte Tin (Sn) plating over Copper (Cu) which has been annealed/heat treated (150°C for 1 hour after plating) - acceptable whisker test results required.
- Tin/Bismuth (Sn/Bi) alloy finishes with 2-5% Bi by weight for use in Lead (Pb)-Free solder reflow processes, or Tin/Bismuth (Sn/Bi) alloy finishes with 2-5% Bi over Copper (Cu) leadframes for Sn/Pb reflow processes.
- Plated Tin/Silver (Sn/Ag) with 2-4% Ag – acceptable test results required.
- Solder-dipped components ($> 1.27\text{mm}$ pitch) using Tin/silver/copper (Sn/Ag/Cu), or Tin/Silver (Sn/Ag).
- Gold (Au) based finishes, and other noble-metal based finishes with the exception of Silver (Ag)-based alloys.
- Plated Nickel/Palladium/Gold (Ni/Pd/Au) over Alloy 42 (Fe-42Ni) leadframes - with acceptable test data.
- Tin/Silver/Copper (Sn/Ag/Cu) alloys used for solder balls are acceptable for Lead (Pb)-Free assembly processes only.
- Tin/Lead (Sn/Pb) is acceptable for Tin/Lead (Sn/Pb) assembly processes, and where specifically allowed by legislation, only.

Unacceptable Termination finish formulations:

- Tin/Copper (Sn/Cu) alloys over any base metal except when applied by hot dip process.
- Tin/Silver/Copper (Sn/Ag/Cu) alloys used for solder balls are not acceptable for Tin/Lead (Sn/Pb) eutectic assembly processes.
- Bright Tin (Sn) plating over any base metal.
- Tin/Bismuth (Sn/Bi) alloy finishes used for Lead (Pb)-Free wave soldering processes (PTH parts) or Sn/Bi over Alloy 42 leadframes used for any eutectic Tin/Lead (Sn/Pb) assembly processes. (Unless approved by both the customer and Celestica process engineering). In addition, any Tin/Bismuth alloys with less than 2% Bi or greater than 5% Bi are not acceptable.
- Tin (Sn) deposits that are compressive during service life are not acceptable.
- Any Tin (Sn)-based plating over Alloy 42 (Fe-42Ni) leadframes without acceptable test data (solderability testing, tin whisker testing, and solder joint reliability testing)
- Any Tin (Sn)-based plating over Copper (Cu) leadframes without approved mitigation practice applied and acceptable test data provided.
- Any Tin (Sn) finishes over copper (no underlayer) with Sn thickness less than 7.62 μm minimum, or 10 μm preferred.
- Any Tin (Sn) finishes over steel.
- Tin/Zinc/Aluminum (Sn/Zn/Al) alloys

- Zinc (Zn)-bearing alloys.
- Indium (In)-bearing alloys.
- Nickel (Ni) – pure nickel finish.
- Silver (Ag) based finishes.
- Hot dipped pure Tin (Sn) finishes.
- Tin/Lead (Sn/Pb) is not acceptable for Lead (Pb)-Free assembly processes, except where specifically allowed by legislation.

Other materials sets and combinations will be considered if they are provided along with strong technical arguments, and are backed up with acceptable test data as per Celestica's requirements.

Note: Any finishes plated over Alloy 42 (Fe-42Ni) leadframes must demonstrate acceptable test results in solderability testing, tin whisker testing, and solder joint reliability testing before they can be considered acceptable by Celestica.

12.2 Separable Connectors

Acceptable termination finish formulations (subject to customer approval):

- Non-tin plating: Nickel/Palladium/Gold or Nickel/Palladium (Ni/Pd/Au or Ni/Pd).
- Non-tin plating: Nickel/Gold (Ni/Au).
- Matte tin (Sn) plating over a Nickel (Ni) underlayer. Components that use Nickel underplating should have a minimum porosity-free Nickel thickness of 0.5 μm with 1.27 μm or greater thickness recommended. (Tin thickness over Ni must be $> 2 \mu\text{m}$).
- Matte Tin (Sn) over a Silver (Ag) underlayer is a potentially acceptable finish with acceptable test results. Minimum required Silver underlayer thickness is 2 μm . (Tin thickness over Ag must be $> 2\mu\text{m}$).
- Fused Tin (Sn) plating. Fusing is a reflowing operation usually done by dipping the Tin-plated surfaces into a hot oil bath shortly after plating. Preferred over nickel barrier (2 μm recommended nickel thickness).
- Hot dipped Tin/Silver (Sn/Ag), or Tin/Silver/Copper (Sn/Ag/Cu). This is a molten tin bath process generally used for structural steel parts, connectors or relays.
- Matte Tin (Sn) plating over Copper (Cu) which has been annealed/heat treated (150°C for 1 hour after plating) - acceptable whisker test results required.
- Tin/Bismuth (Sn/Bi) alloy finishes with 2-5% Bi by weight over a Nickel (Ni) underlayer for surface-mounted connectors. Minimum porosity-free Nickel thickness of 0.5 μm with 1.27 μm or greater thickness recommended.
- Plated Tin/Silver (Sn/Ag) with 2-4% Ag over a Nickel (Ni) underlayer. Minimum porosity-free Nickel thickness of 0.5 μm with 1.27 μm or greater thickness recommended.
- Gold (Au) based finishes, other noble-metal based finishes with the exception of silver-based types.
- Tin/Lead (Sn/Pb) is acceptable for Tin/Lead (Sn/Pb) assembly processes, and where specifically allowed by legislation, only.

Unacceptable Termination finish formulations:

- Tin/Copper (Sn/Cu) alloys (unless applied via hot-dip process and with acceptable test data).
- Bright Tin (Sn) plating over any base metal.
- Tin/Bismuth (Sn/Bi) alloy finishes with less than 2% Bi or greater than 5% Bi are not acceptable. This finish is not recommended for wave-soldered parts.
- Tin (Sn) deposits that are compressive during service life are not acceptable.
- Any Tin (Sn)-based plating over Alloy 42 (Fe-42Ni) leadframes without acceptable test data.
- Any Tin (Sn)-based plating (matte or bright) over Copper (Cu) leadframes without approved mitigation practice applied and acceptable test data provided.
- Hot dipped pure Tin (Sn) finishes.
- Tin/Lead (Sn/Pb) is not acceptable for Lead (Pb)-Free assembly processes.

Other materials sets and combinations will be considered if they are provided along with strong technical arguments, and are backed up with acceptable test data as per Celestica's requirements.

12.3 Bus Bars

Acceptable bus bar finish formulations (subject to customer approval):

- Unplated (no applied finish) is acceptable for aluminum or copper alloys. Note this is not a solderable finish.
- Nickel (Ni) is acceptable for copper alloys or for aluminum if used over copper strike plating. Note this is not a solderable finish.
- Chromium (Cr) (excluding hexavalent chromium Cr VI, Cr⁶⁺) is acceptable for copper alloys or for aluminum if used over copper strike plating. Note this is not a solderable finish.
- Solder-dipped Tin/Silver/Copper (Sn/Ag/Cu) is acceptable for copper alloys or for aluminum if used over copper strike plating. This is a solderable finish.
- Silver (Ag) applied by immersion or electroplate method is acceptable for copper alloys or for aluminum if used over copper strike plating. This is a solderable finish.

Unacceptable bus bar finish formulations:

- Nickel (Ni) over aluminum if used without copper strike plating. Note this is not a solderable finish.
- Chromium (Cr) (excluding hexavalent chromium Cr VI, Cr⁶⁺) over aluminum if used without copper strike plating. Note this is not a solderable finish.
- Solder-dipped Tin/Silver/Copper (Sn/Ag/Cu) over aluminum if used without copper strike plating. This is a solderable finish.
- Silver (Ag) applied by immersion or electroplate method over aluminum if used without copper strike plating. This is a solderable finish.
- Matte Tin (Sn) used over copper or aluminum alloys. This is a solderable finish.

Other materials sets and combinations will be considered if they are provided along with strong technical arguments, and are backed up with acceptable test data as per Celestica's requirements.

12.4 Heat Sinks

Acceptable heat sink finish formulations (subject to customer approval):

- Unplated (no applied finish, or anodizing for aluminum) is acceptable for aluminum, copper, or graphite base materials. Note this is not a solderable finish.
- Nickel (Ni) is acceptable for copper base materials. Note this is not a solderable finish.
- Tin/Silver/Copper (Sn/Ag/Cu) is acceptable for copper base material or aluminum if used over copper strike plating. This is a solderable finish.
- Matte Tin (Sn) over Nickel (Ni) is acceptable for copper base material or aluminum if used over copper strike plating. This is a solderable finish. Acceptable tin whisker test results are required for this finish.

Unacceptable heat sink finish formulations:

- Matte Tin (Sn) used over copper or aluminum base materials. This is a solderable finish.

Other materials sets and combinations will be considered if they are provided along with strong technical arguments, and are backed up with acceptable test data as per Celestica's requirements

12.5 Printed Circuit Boards (PCB's)

Acceptable PCB finish formulations (subject to customer approval):

- Immersion Tin (Sn). Acceptable tin whisker test results are required for this finish.
- Electroless Nickel (Ni) / Immersion Gold (Au) (ENIG).
- Electroless Nickel (Ni) / Electroless Palladium (Pd) / Immersion Gold (Au) (ENEPIG).
- Electroplated Nickel (Ni) / Electroplated Gold (Au).
- Immersion Silver (Ag).
- OSP.

Unacceptable PCB finish formulations:

- Copper (Cu).
- Tin/Copper (Sn/Cu) HASL. Acceptable tin whisker test results are required for this finish.

12.6 Non-Soldered Part Compatibility

12.7 Metal Finishing

Note that all passivation finish recommendations listed below are based primarily on chemical compatibility with RoHS material restrictions. Any formulations to be used for Celestica products require full testing and approvals to be completed prior to shipment to Celestica.

Acceptable Fastener (Nuts, bolts, screws, lugs) passivation finish formulations (subject to customer approval):

- Passivation methods must not utilize RoHS restricted materials above the thresholds as defined in the Directive 2002/95/EC. Potential options:
 - Stainless Steel
 - Clear/Blue finish – Trivalent Chromium
 - Green finish – Trivalent Chromium
 - Yellow finish – Trivalent Chromium
 - Black finish – Trivalent Chromium
 - Black finish – Silver/Nickel
 - Thick Film – Trivalent Chromium
 - Thin Film – Trivalent Chromium
- Finishes should also be able to withstand 96 hours salt spray test (per ASTM B117), and have a maximum surface resistivity of 5 mohms/in².

Unacceptable Fastener (Nuts, bolts, screws, lugs) passivation finish formulations:

- Any finishes containing Hexavalent Chromium are unacceptable. Known types include:
 - Yellow finish / Spectrum finish – Zinc Chromate – Hexavalent Chromium
 - Bronze finish – Zinc Chromate – Hexavalent Chromium
 - Olive Drab finish – Zinc Chromate – Hexavalent Chromium
 - Black finish – Zinc Chromate – Hexavalent Chromium
 - Clear/Blue finish – Zinc Chromate – Hexavalent Chromium

Acceptable Sheet Steel passivation finish formulations (subject to customer approval):

- Passivation methods must not utilize RoHS restricted materials above the thresholds as defined in the Directive 2002/95/EC. Potential options:
 - Stainless steel
 - Clear/Blue finish – Trivalent Chromium
 - Thick Film – Trivalent Chromium
- Finishes should also be able to withstand 96 hours salt spray test (per ASTM B117), and have a maximum surface resistivity of 5 mohms/in².

Unacceptable Sheet Steel passivation finish formulations:

- Any finishes containing Hexavalent Chromium are unacceptable. Known types include:
 - Yellow finish / Spectrum finish – Zinc Chromate – Hexavalent Chromium
 - Clear/Blue finish – Zinc Chromate – Hexavalent Chromium

Acceptable Aluminum (Sheets, Extrusions, and Castings) passivation finish formulations (subject to customer approval):

- Passivation methods must not utilize RoHS restricted materials above the thresholds as defined in the Directive 2002/95/EC. Potential options:
 - Alodine 5200
 - Alodine 5700
 - Alodine 4595 – Trivalent Chromium

- Chemetall AL-0500 – Trivalent Chromium
- APS TCP-HF – Trivalent Chromium
- Passivation methods which result in a color change to the aluminum are preferred by Celestica Inc. Finishes should also be able to withstand 168 hours salt spray test (per ASTM B117).

Unacceptable Aluminum (Sheets, Extrusions, and Castings) passivation finish formulations:

- Any finishes containing Hexavalent Chromium are unacceptable. Known types include:
 - Alodine 1000 – Hexavalent Chromium
 - Alodine 1200 – Hexavalent Chromium

12.8 Plastic Parts

Note that all plastic material recommendations listed below are based primarily on chemical compatibility with RoHS material restrictions. Any formulations to be used for Celestica products require full testing and approvals to be completed prior to shipment to Celestica.

Acceptable Plastic part formulations (subject to customer approval):

- Any formulations which do not utilize RoHS restricted materials above the thresholds as defined in the Directive 2002/95/EC, and also meet the flammability requirements of UL 94V-0

Unacceptable Plastic part formulations:

- Any formulations which utilize RoHS restricted materials above the thresholds as defined in the Directive 2002/95/EC

12.9 General Note

Other materials sets and combinations will be considered if they are provided along with strong technical arguments, and are backed up with acceptable test data as per Celestica's requirements